## Application

The module 513DIR01 provides 16 galvanic isolated inputs for up to 16 binary process signals. Scanning and processing of the inputs are executed with the high time resolution of 1 ms. The allocation of an input signal to the processing functions can be done according to the rules of configuration.

The module 513DIR01 is able to process the following types of signals or a combination of them:

- 16 single point information with time stamp (SPI)
- 8 double point information with time stamp (DPI)
- 2 digital measured values each with 8 bit (DMI8)
- 1 digital measured value with 16 bit (DMI16)
- 16 integrated totals (max. 120 Hz) (ITI)
- 2 step position information each with 8 bit (STI)
- 2 bitstring input each with 8 bit (BSI8)
- 1 bitstring input with 16 bit (BSI16)
- or combinations of this signal types

## The module is available in one version:

513DIR01 : process voltage 24 to 60 V DC.
LED signaling for each input, common return per 8 inputs.





## Characteristics

### **Binary inputs**

The inputs are galvanic isolated by means of optical couplers. 8 inputs are building a group with a common return. The input circuit is designed to keep the input current constant by using current regulative diodes.

The binary input channels are protected against reverse voltage installation. If the input signal is installed with wrong polarity the input current will be zero.

The module has 16 LEDs to indicate the signal state at the inputs. The LEDs follow direct the input signal.

The maximum permissible frequency for counter pulses is 120 Hz.

### Power supply input

The required power for the module is supplied via the RTU513 backplane.



Figure 1: Block diagram 513DIR01

## I/O controller (IOC)

The micro-controller on the module processes all time critical tasks of the parameterized processing functions. Moreover it carries out the interactive communication with the RTU I/O bus. All configuration data and processing parameters are loaded by the communication unit via the RTU I/O bus.

The module is equipped with a serial interface to the RTU513 I/O bus on the backplane.

The binary input unit can execute the following processing functions for the different types of signals:

- Digital filtering to suppress contact bounce
- Suppression of oscillating signals caused by the process



## Characteristics

- Validity check and suppression of intermediate input states for double indications
- Consistancy check for all channels allocated to digital measured values or step position information
- Summation of increment pulses to form integrated totals in registers of 31 bit resolution
- Copying of integrated totals values into freezing registers for data conservation

The module provides a data buffer for temporally storing of up to 50 event messages including time stamps. The events are stored in chronological order designated for transmission to the communication unit (CMU).

During initialization and operation the module carries out a number of tests. If a fault occurs it is reported to the communication unit. All fault conditions impairing the function of the module are displayed as common fault signal by a red LED. A failure of the module is detected by the communication unit.

## Operation

The binary input module 513DIR01 provides 16 galvanic isolated inputs for up to 16 binary process signals. Scanning and processing of the inputs are executed with the high time resolution of 1 ms. The allocation of an input signal to the processing functions can be done according to the rules of configuration.

## Setting

The device has no switches or jumpers.

## Signaling

The 513DIR01 has 16 yellow LED's on the front plate indicating the state of the inputs. The LED's indicate the signal after the conversion by the optocouplers. So the LED's follow the signal directly.

## LED ERR

The module monitors and checks the own functionality as well as the dialog via the I/O bus. Detected errors are



## Setting

indicated by the red LED ERR on the front plate and transmitted via the I/O bus to the communication unit (CMU). Additional diagnostic messages are available using the Web-Server on the CMU.

The LED ERR indicates module errors or I/O bus errors:

- module runs initialization procedure
- module is performing a cold or warm start
- module has detected a memory error (RAM or Flash)
- micro-controller is faulty
- no dialog via the I/O bus for at least 2 minutes. The module is not polled by the CMU

### Connections

### **Connection to RTU513 racks**

The module is intended to be used in an RTU513 rack. The connection to the power supply, the RTU513 I/O bus and the process terminals is done via the backplane connector in the RTU513 rack.

### Compatibility

The 513DIR01 module is plug and functional compatible to the module 16DIM10 used in RTU513. It can replace this modules.

### Safety instructions

### Dangerous process voltages

Hazardous voltage.

Contact with live circuits will cause electric shock or burn.

Verify that all terminals feeding dangerous contact voltages (supply voltage, input or output channels) is in secure OFF state before connecting or withdrawing the terminals.



## Connections

| WMS11         | SMS12         |         |                | Signal Identification |   |
|---------------|---------------|---------|----------------|-----------------------|---|
| WMS12         | Sub-connector |         | Identification | Meaning               |   |
| 1             |               |         | z32            | I01                   | Binary input 1 +                            |
| 2             |               | b32     |                | 102                   | Binary input 2 +                            |
| 3             | d32           |         |                | 103                   | Binary input 3 +                            |
| 4             |               |         | z30            | 104                   | Binary input 4 +                            |
| 5             |               | b30     |                | 105                   | Binary input 5 +                            |
| 6             | d30           |         |                | 106                   | Binary input 6 +                            |
| 7             |               |         | z28            | 107                   | Binary input 7 +                            |
| 8             |               | b28     |                | 108                   | Binary input 8 +                            |
| 9             | d28           |         |                | 109                   | Binary input 9 +                            |
| 10            |               |         | z26            | I10                   | Binary input 10 +                           |
| 11            |               | b26     |                | l11                   | Binary input 11 +                           |
| 12            | d26           |         |                | l12                   | Binary input 12 +                           |
| 13            |               |         | z24            | I13                   | Binary input 13 +                           |
| 14            |               | b24     |                | l14                   | Binary input 14 +                           |
| 15            | d24           |         |                | I15                   | Binary input 15 +                           |
| 16            |               |         | z22            | I16                   | Binary input 16 +                           |
| 17            |               | A       |                | A                     | Not used                                    |
| 18            |               | b22     |                | C01                   | Connection return input 1 to 8 -            |
| 19            | d22           |         |                | C02                   | Connection return input 9 to 16 -           |
| The pin confi | guration      | of SMS1 | 2 Subrac       | k corresponds to th   | ne pin configuration of the board connector |



## Connections



Figure 2: Placement of signal terminal connectors on subracks

| •   |
|---|
| 513DIR01  |
|   |
| ERR   |
| 1<br>2<br>3<br>4<br>5<br>6<br>7<br>8<br>9<br>10<br>11<br>12<br>13<br>14<br>15<br>16 |
| PKS   |
|   |

Front plate



## Connections



Figure 3: Process connection



## **Technical Data**

In addition to the PKS RTU 513 general technical data, the following applies:

#### Binary input channels 513DIR01

| Input   | 16channels,<br>common return for 2 groups of 8 chan-<br>nels, isolated by opto-couplers |
|---|---|
| Nominal input voltage                         | 24 60 V DC (+/- 20%)  |
| Max. input voltage                            | 72 V DC   |
| Input current                                 | 1.8 2.2 mA constant   |
| Logical '1' definitely detected               | >=18 VDC  |
| Logical '0' definitely<br>detected            | >=9 VDC   |
| Reverse voltage<br>protection                 | yes   |
| Max. input frequency for<br>integrated totals | 120Hz   |

# Current consumption for power supplied via RTU513 backplane

| 5V DC  | 100 mA |
|--------|--------|
| 24V DC | -      |

### Signaling by LEDs

| ERR (red) | Common fault information for the module |
|-----------|---|
| 1 16      | LED displays the active inputs          |

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| ERR (red) | Common fault information for the module |  |
|-----------|---|--|
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### **Mechanical layout**

| Dimensions   | mm x 100 mm, 3HE איז<br>euro card format<br>4R (20 mm) front panel |
|--------------|--|
| Housing type | Printed circuit board  |

### **Mechanical Layout**

| PCB    | 160 x 100 mm |
|--------|--------------|
| Weight | App. 0.3 kg  |

#### **Environmental conditions**

| Temperature       | -25 70 °C                  |
|-------------------|----------------------------|
| Relative humidity | 5 95 %<br>(non condensing) |

### Insulation tests

| AC test voltage<br>IEC 61000-4-16<br>IEC 60870-2-1 (class VW3)                 | 2.5kV, 50 Hz<br>Test duration: 1 min |
|--|--------------------------------------|
| Impulse voltage withstand<br>test IEC 60255-5<br>IEC 60870-2-1 (class VW<br>3) | 5 kV (1.2 / 50 μs)                   |
| Insulation resistance<br>IEC 60255-5   | > 100 MΩ at 500 V DC                 |



#### PKS RTU513

## **Technical Data**

### Immunity test

| Electrostatic discharge<br>IEC 61000-4-2  | 8 kV air / 6 kV contact<br>(level 3)<br>Performance criteria A |
|---|--|
| Radiated Radio-Frequency<br>Electromagnetic Field<br>IEC 61000-4-3              | 10 V/m (level 3)<br>Performance criteria A                     |
| Electrical Fast Transient /   | 4 kV (level X)   |
| Burst IEC 61000-4-4   | Performance criteria A   |
| Surge   | 4 kV (level 4)   |
| IEC 61000-4-5   | Performance criteria A   |
| Conducted Disturbances,<br>induced by<br>RadioFrequency Fields<br>IEC 61000-4-6 | 10 V (level 3)<br>Performance criteria A                       |
| Damped oscillatory wave   | 2.5 / 1 kV (level 3)   |
| IEC 61000-4-18  | Performance criteria A   |

